Inventor Name Search Result

Your Search was:

Last Name = CABEZAS
First Name = RAFAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10758859	Not Issued	020	01/16/2004	DUPLICATE NETWORK ADDRESS DETECTION	CABEZAS, RAFAEL GRANIELLO
10703022	Not Issued	071	11/06/2003	ANTI-THEFT METHOD AND SYSTEM FOR PORTABLE ELECTRONIC DEVICES	CABEZAS, RAFAEL GRANIELLO
10703019	Not Issued	030	11/06/2003	METHOD AND SYSTEM FOR MULTIPLE INSTANT MESSAGING LOGIN SESSIONS	CABEZAS, RAFAEL GRANIELLO
10621947	Not Issued	030	07/17/2003	SYSTEM AND METHOD FOR ON-DEMAND COMPUTING FOR PARALLEL SCSI	CABEZAS, RAFAEL GRANIELLO
10405375	Not Issued	030	04/01/2003	METHOD AND APPARATUS FOR TRACING TROUBLESHOOTING EVENTS FOR AIDING TECHNICAL ASSISTANCE	CABEZAS, RAFAEL GRANIELLO
10347837	Not Issued	030	01/17/2003	ELECTRONIC MESSAGING SYSTEM AND METHOD WITH AUTOMATIC PROMPTING	CABEZAS, RAFAEL GRANIELLO
10347773	Not Issued	030	01/17/2003	METHOD, APPARATUS, AND PROGRAM FOR TRANSMITTING TEXT MESSAGES FOR SYNTHESIZED SPEECH	CABEZAS, RAFAEL GRANIELLO
09951948	Not Issued	168	09/13/2001	PORTABLE SCSI BUS ANALYZER	CABEZAS, RAFAEL GRANIELLO
09903946	Not Issued	071	07/12/2001	UNIFIED DIAGNOSTICS PLATFORM SYSTEM AND METHOD FOR EVALUATING COMPUTER PRODUCTS	CABEZAS, RAFAEL G.
09737455	Not Issued	121	12/14/2000	METHOD AND APPARATUS FOR ENHANCED POWER CONSUMPTION HANDLING OF BUS-CONTROLLED COMPONENTS	CABEZAS, RAFAEL G.
09730406	6745345	150	12/04/2000	METHOD FOR TESTING A COMPUTER BUS USING A BRIDGE CHIP HAVING A FREEZE-ON-ERROR OPTION	CABEZAS, RAFAEL G.
09620724	6662320	150	07/20/2000	METHOD AND APPARATUS FOR INHIBITING AN ADAPTER BUS ERROR SIGNAL FOLLOWING A RESET OPERATION	CABEZAS, RAFAEL GRANIELLO

09435072	Not Issued	168	11/04/1999	APPARATUS AND METHOD TO AUTOMATICALLY INITIATE A COMPUTER PROGRAM BASED ON A DETECTED PRESENCE OF A USER	CABEZAS, RAFAEL GRANIELLO
09103781	6426962	150	06/24/1998	TOKEN RING JITTER GENERATOR/DETECTOR	CABEZAS, RAFAEL GRANIELLO
08430080	5612961	150	04/27/1995	METHOD AND SYSTEM FOR VERIFICATION OF THE BAUD RATE FOR AN ASYNCHRONOUS SERIAL DEVICE RESIDING WITHIN A DATA PROCESSING SYSTEM	CABEZAS, RAFAEL G.

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** CABEZAS **First Name** RAFAEL Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

Inventor Name Search Result

Your Search was:

Last Name = KNABENBAUER
First Name = DANIEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09903946	Not Issued	071	07/12/2001	UNIFIED DIAGNOSTICS PLATFORM SYSTEM AND METHOD FOR EVALUATING COMPUTER PRODUCTS	KNABENBAUER, DANIEL J.
09737455	Not Issued	121	12/14/2000	METHOD AND APPARATUS FOR ENHANCED POWER CONSUMPTION HANDLING OF BUS-CONTROLLED COMPONENTS	KNABENBAUER, DANIEL J.
09477571	Not Issued	124	01/06/2000	THREE-DIMENSIONAL DISPLAY APPARATUS	KNABENBAUER, DANIEL J.
09477570	Not Issued	124	01/06/2000	THREE-DIMENSIONAL DISPLAY APPARATUS	KNABENBAUER, DANIEL J.
09477568	6479929	150	01/06/2000	THREE-DIMENSIONAL DISPLAY APPARATUS	KNABENBAUER, DANIEL J.

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** KNABENBAUER **First Name** DANIEL Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	(("5684789") or ("4165443")).PN.	USPAT	OR	OFF	2005/08/17 16:47
S2	0	diagnostic and switch and multiplexor and scsi and compatability	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 16:49
S3	5767	multi-function and switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 16:49
S4	166	S3 and multiplexor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 16:49
S5	53	S4 and diagnostics	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 17:30
S6	7103	(computer same peripheral) and diagnostics	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 17:32
S7	3684	S6 and (operating adj system)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 17:32
S8	125	S7 and multiplexor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 17:34
S9	26	S8 and SCSI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 17:37

S10	99	S7 and peripheral.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 17:37

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	607	(703/22,24).CCLS.	USPAT	OR	OFF	2005/08/18 13:29

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	897	(702/186,183).CCLS.	USPAT	OR	OFF	2005/08/18 11:04
L2	149	1 and peripheral	USPAT	OR	ON	2005/08/18 11:05
L3	59	2 and diagnostic$3	USPAT	OR	ON	2005/08/18 11:05

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("4837764").PN.	USPAT	OR	OFF	2005/08/18 11:38
L2	41	("3711691" | "4168796" | "4489414" | "4567592" | "4608531" | "4718064" | "4724378" | "4736374").PN. OR ("4837764").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/08/18 11:38
L3	22	("4837764" | "4964124" | "5247683" | "5410681" | "5421009" | "5495584" | "5504689" | "5748575" | "5794052" | "5802297" | "5805897" | "5835777" | "5842024" | "5894571" | "5995757" | "6178527" | "6182275" | "6236901" | "6389560" | "6442712" | "6543047" | "6751569").PN. OR ("6931575").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/08/18 11:44

Published before July 2001
Terms used
computer peripherals diagnostics multiple operating systems

Found **204** of **116,947**

Sort results by: relevance
Display results: expanded form

Save results to a Binder
Search Tips
☐ Open results in a new window

Try an Advanced Search
Try this search in The ACM Guide

Results 1 - 20 of 200 Result page: **1** 2 3 4 5 6 7 8 9 10 next
Best 200 shown

Relevance scale

1 Message communication protocol and operating system design for the Distributed Loop Computer Network (DLCN)
Ming T. Liu, Cecil C Reames
March 1977 **ACM SIGARCH Computer Architecture News , Proceedings of the 4th annual symposium on Computer architecture**, Volume 5 Issue 7
Full text available: pdf(717.65 KB) Additional Information: full citation, abstract, references, citings, index terms

The Distributed Loop Computer Network (DLCN) is envisioned as a powerful, unified distributed computing system which interconnects midi/mini/micro-computers, terminals and other peripherals through careful integration of hardware, software and a loop communication network. Research concerning DLCN has concentrated on the loop communication network, message protocol and distributed network operating system. For the loop communication network, previous papers [2,3] reported a novel message tr ...

2 Order processing and inventory control software related to computer user satisfaction: an interactive online evaluation system
Avi Rushinek, Sara Rushinek
May 1985 **Proceedings of the 1985 ACM SIGSMALL symposium on Small systems**
Full text available: pdf(869.95 KB) Additional Information: full citation, abstract, references

The selection of an order processing and inventory control (OPICS) system is a complicated process. The overall satisfaction derived from a system depends on many variables. This study analyzes the influence of OPICS predictor variables on overall satisfaction as determined by multiple regression. This study confirms the theories that suggest that OPICS ease of operation, reliability of computer, and ease of programming are the major determinants of overall computer user satisfaction. ...

3 A framework for the assessment of operating systems for small computers
Hossein Saiedian, Munib Siddiqi
April 1996 **ACM SIGICE Bulletin**, Volume 21 Issue 4
Full text available: pdf(1.89 MB) Additional Information: full citation, abstract, references, index terms

A number of high performance operating systems are now available for small computers on different hardware platforms. These operating systems offer many advanced features formerly reserved for their workstation and minicomputer counterparts. This article surveys the most widely used of such operating systems, namely OS/2, Windows NT, Linux and Macintosh System 7.5. It provides an account on the history, design objectives and evolution of these operating systems and discusses their key features, ...

Keywords: CP/M, DOS, Linux, Macintosh, Microcomputers, OS/2, Operating Systems, Small Computer Systems, Windows, Windows NT

4 Three-dimensional medical imaging: algorithms and computer systems
M. R. Stytz, G. Frieder, O. Frieder
December 1991 **ACM Computing Surveys (CSUR)**, Volume 23 Issue 4
Full text available: pdf(7.38 MB) Additional Information: full citation, references, citings, index terms, review

Keywords: Computer graphics, medical imaging, surface rendering, three-dimensional imaging, volume rendering

5 EASY—an operating system for the QM-1
Charles W. Flink
September 1977 **ACM SIGMICRO Newsletter , Proceedings of the 10th annual workshop on Microprogramming**, Volume 8 Issue 3
Full text available: pdf(733.19 KB) Additional Information: full citation, abstract, references, citings, index terms

The Emulation Aid SYstem is a virtual machine monitor for the Nanodata QM-1 microprogrammable computer. The system is designed to provide the user with an interactive interface for the development and subsequent use of emulations on the QM-1. EASY provides integrated support for: 1) interactive control of multiple, concurrently resident, virtual computers implemented via emulation, 2) input/output from emulations (virtual I/O) to the various real peripherals of the QM-1, and 3) diagnostic d ...

Keywords: Emulation, Intermediate language machines, Microprogramming, Nanodata QM-1, Software engineering, Virtual machine monitors, Virtual machines

6 The Soviet Bloc's Unified System of Computers
N. C. Davis, S. E. Goodman
June 1978 **ACM Computing Surveys (CSUR)**, Volume 10 Issue 2
Full text available: pdf(2.76 MB) Additional Information: full citation, references, citings, index terms

7 Special issue: AI in engineering
D. Sriram, R. Joobbani
January 1985 **ACM SIGART Bulletin**, Issue 91
Full text available: pdf(8.79 MB) Additional Information: full citation, abstract

The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

8 System architectures for computer music
John W. Gordon
June 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 2
Full text available: pdf(4.61 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Computer music is a relatively new field. While a large proportion of the public is aware of computer music in one form or another, there seems to be a need for a better understanding of its capabilities and limitations in terms of synthesis, performance, and recording hardware. This article addresses that need by surveying and discussing the architecture of existing computer music systems. System requirements vary according to what the system will be used for. Common uses for co ...

9 A flow oriented computer system simulation language
William R. Franta
January 1971 **Proceedings of the 1971 26th annual conference**

Full text available: pdf(1.02 MB) Additional Information: full citation, abstract, references, index terms

This paper describes a language expressly designed for the simulation of operating system software. A carefully selected portion of the modeling machinery has been incorporated into the language so that the user need only be concerned with the operating system logic in constructing the modeling program. The modeling statements, as input to the assembly portion of the package result in the generation of a series of tables and pseudo code segments which are acted upon interpretatively during ...

Keywords: Flow-oriented, Multiprocessing systems, Multiprogramming, Operating system development, Simulation languages, Simulation of operating systems

10 Experience Using Multiprocessor Systems—A Status Report 
Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2

Full text available: pdf(4.48 MB) Additional Information: full citation, references, citings, index terms

11 Curriculum 68: Recommendations for academic programs in computer science: a report of the ACM curriculum committee on computer science 
William F. Atchison, Samuel D. Conte, John W. Hamblen, Thomas E. Hull, Thomas A. Keenan, William B. Kehl, Edward J. McCluskey, Silvio O. Navarro, Werner C. Rheinboldt, Earl J. Schweppe, William Viavant, David M. Young
March 1968 **Communications of the ACM**, Volume 11 Issue 3

Full text available: pdf(6.63 MB) Additional Information: full citation, references, citings

Keywords: computer science academic programs, computer science bibliographies, computer science courses, computer science curriculum, computer science education, computer science graduate programs, computer science undergraduate programs

12 Special section: Reasoning about structure, behavior and function 
B. Chandrasekaran, Rob Milne
July 1985 **ACM SIGART Bulletin**, Issue 93

Full text available: pdf(5.13 MB) Additional Information: full citation, abstract, references

The last several years' of work in the area of knowledge-based systems has resulted in a deeper understanding of the potentials of the current generation of ideas, but more importantly, also about their limitations and the need for research both in a broader framework as well as in new directions. The following ideas seem to us to be worthy of note in this connection.

13 Reliability Issues in Computing System Design 
B. Randell, P. Lee, P. C. Treleaven
June 1978 **ACM Computing Surveys (CSUR)**, Volume 10 Issue 2

Full text available: pdf(3.95 MB) Additional Information: full citation, references, citings, index terms

14 Curriculum recommendations for graduate professional programs in information systems 
May 1972 **Communications of the ACM**, Volume 15 Issue 5

Full text available: pdf(4.00 MB) Additional Information: full citation, references, citings

Keywords: education, information analysis, information systems development, management information systems, management systems, system design, systems analysis

15 Hardware monitoring of real-time aerospace computer systems

D. R. Partridge, R. E. Card

March 1976 **Proceedings of the 1976 ACM SIGMETRICS conference on Computer performance modeling measurement and evaluation**

Full text available: pdf(1.00 MB) Additional Information: full citation, abstract, index terms

Hardware monitoring has proven to be a useful means for measuring the performance of computer systems generally, and is particularly attractive for use on real-time systems due to its attribute of non-interference with system operation. This technique is uniquely able to quantify precisely the interactions between hardware and software, which must be completely understood in these systems. In this paper, we report the application of a commercially-developed hardware monitor to two real-time ...

16 The Microprogrammable Multi-Processor (MMP) system for simultaneous emulation of interoperating computer systems

Roy Mattson, Alan Salisbury

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

Full text available: pdf(647.32 KB) Additional Information: full citation, abstract, references, citings, index terms

A Teleprocessing Design Center (TDC) has been established within the Communications/Automatic Laboratory of the US Army Electronics Command (ECOM) at Fort Monmouth, New Jersey, for the purpose of supporting experimentation in developing and validating Army Tactical Data Systems (ARTADS) configurations. The TDC is collocated within the Communications/Automatic Data Processing Laboratory with the Communications System Design Facility which includes circuit and message switching sys ...

17 The gould NP1 system interconnecting

D. J. Vianney, J. H. Thomas, V. Rabaza

June 1988 **Proceedings of the 2nd international conference on Supercomputing**

Full text available: pdf(1.28 MB) Additional Information: full citation, abstract, references, index terms

The Gould NP1 is a multicomputer multiprocessing system designed for high performance and parallel processing required in diverse scientific and engineering applications. The NP1's basic building block is a dual-processor single bus system which can be expanded up to eight processors over four system buses. This paper discusses the overall design and implementation of tne NP1 system interconnection in particularly the inter-system bus link which interconnects four system buses to ...

18 Jericho: A professional's personal computer system

Norton R. Greenfeld

May 1981 **Proceedings of the 8th annual symposium on Computer Architecture**

Full text available: pdf(631.08 KB) Additional Information: full citation, abstract, references

Jericho is a reactive computing engine capable of supporting advanced high user-intensity applications systems. It has four primary architectural characteristics: 1. A user-dedicated computing environment 2. Direct execution of high-level programming languages 3. High bandwidth communication between user and system 4. High bandwidth communication between user-support machines and the other machines the task environment. Each of these ar ...

19 OMP: a RISC-based multiprocessor using orthogonal-access memories and multiple spanning buses

K. Hwang, M. Dubois, D. K. Panda, S. Rao, S. Shang, A. Uresin, W. Mao, H. Nair, M. Lytwyn, F. Hsieh, J. Liu, S. Mehrotra, C. M. Cheng

June 1990 **ACM SIGARCH Computer Architecture News , Proceedings of the 4th international conference on Supercomputing**, Volume 18 Issue 3

Full text available: pdf(1.96 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper presents the architectural design and RISC based implementation of a prototype supercomputer, namely the Orthogonal MultiProcessor (OMP). The OMP system is

constructed with 16 Intel 1860 RISC microprocessors and 256 parallel memory modules, which are 2-D interleaved and orthogonally accessed using custom-designed spanning buses. The architectural design has been validated by a CSIM-based multiprocessor simulator. The design choices are based on worst-case delay a ...

20 Computer system simulation of an on-line interactive command and control system

Herman Fischer

January 1971 **Proceedings of the 5th conference on Winter simulation**

Full text available: pdf(617.19 KB) Additional Information: full citation, abstract, index terms

A computer simulation model was used as an analysis "tool" for computer system design trade-offs for an on-line interactive command and control system preliminary design study project. Three basic hardware configurations were modelled at the hardware interrupt/byte flow level: a. A Centralized Dual Multiprocessor b. Dual Computers c. A Distributed System of Central and Remote Computers The software of the system was modelled ...